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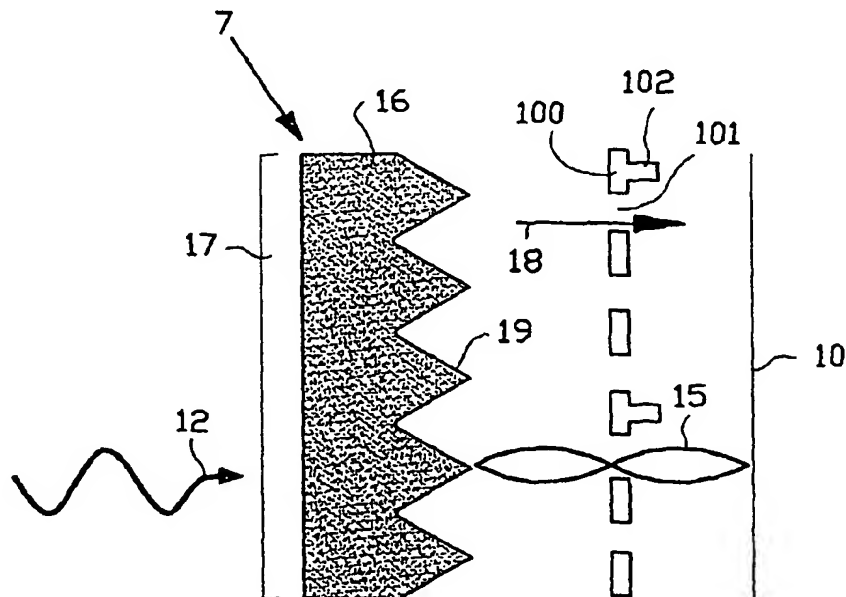
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(54) Title: LITHOGRAPHY SYSTEM COMPRISING A PROTECTED CONVERTER PLATE



(57) Abstract: A lithography system comprising a converter element (7) for receiving light and converting said light in a plurality of electron beams (15) to be directed towards and focused on a substrate (10) to be processed, said plurality of electron beams (15) being used to define a pattern in a resist layer (20) on said substrate (10), wherein said lithography system is provided with a protective foil with holes at the position of the electron beams (23) being arranged to protect, in use, said converter element (7) from contamination with material from the resist layer (21).

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LITHOGRAPHY SYSTEM COMPRISING A PROTECTED CONVERTER PLATE

Lithography system comprising a converter plate and means for protecting the
5 converter plate

Field of the invention

The invention relates to a lithography system comprising a converter element for
receiving light and converting said light in a plurality of electron beams to be directed
10 towards and focused on a substrate to be processed. The invention further relates to a
method for preventing contamination of a source of charged particles in a lithography
system with material from a resist layer present on a substrate to be processed. The
invention further relates to a substrate processed using the lithography system
described.

15

Prior art

A lithography system using a converter element designed to convert a light beam
into beams of charged particles is known per se from WO98/54620, which text is
incorporated herein as if fully set forth. The purpose of the use of the converter element is
20 to provide a better resolution (0.1 μm or less) than was possible with prior art systems
without such converters in which the resolution was entirely determined by the
wavelength of the light beam used. Furthermore, such a lithography system makes it
possible to obtain a high productivity. The converter element, preferably being a
semiconductor field-emission photo cathode array, releases a plurality of electron
25 beams. The electron beams are released from the tips of semiconductor needles. The
electron beams are thus released from relatively small areas. In practice, the area of a
tip only takes up less than 0.1 % of the total area of the converter element.

Despite the fact that the converter element in such a lithography system is
sturdy and not prone to damage, contamination was found to reduce the quality of the
30 sources, and inherently, to limit the minimal feature size to be written on the substrate.
Contamination occurs due to the interaction of electrons from the source with the resist

layer that is needed for pattern definition on the substrate's surface. The resist layer, for instance being a polymer, degrades by the energy release from the impinging electrons. The polymer molecules crack and parts of them may be expelled from the surface of the resist layer. By traversing the vacuum, the molecules may reach the surface of the field emitter array and adsorb there, causing a reduction of the quality of the emitters. Also, if other resists are used, material originating from the surface may become freed and reach the surface of the converter element, causing reduction of the quality. Furthermore, also other contaminations present in the system affect the converter surface and its performance.

Since the precision of lithography systems moves towards a sub 0.1 micron resolution, contamination of charged particle sources becomes more important.

US-patent 4,902,930 describes an arrangement of an extremely thin protective foil in an electron image projector. The foil is used as a vacuum seal halfway between the electron source and the substrate to prevent gas, liberated from the target by the impinging electron beam, from reaching the electron source. The foil is permeable for electrons though. However, in a lithography system of the sub 0.1 micron generation, this system will not work. Electrons will scatter when passing the foil. Even when the scattering angle is very small, sub 0.1 micron resolution cannot be reached anymore. Another problem with this known solution for a contamination problem is the fact that the foil needs to be extremely thin, i.e. < 0.1 micron. In high electric fields, as used in lithography systems as described in WO98/54620, the electrostatic force will deform the foil. As a result the presently required sub-0.1 micron resolution cannot be obtained.

Summary of the invention

It is an object of the present invention to provide a method and an arrangement for a lithography system to accomplish the definition of feature sizes, smaller than the diffraction limited wavelength of the light source used by improving the reliability of the surface quality of the electron sources in the field emitter array.

The present invention relates to a lithography system comprising a converter element for receiving light and converting said light in a plurality of electron beams to be directed towards and focused on a substrate to be processed, said plurality of electron beams being used to define a pattern in a resist layer on said substrate, wherein

said lithography system is provided with a protective thin foil located between said converter element and said substrate and having holes at the positions of the electron beams and being arranged to protect, in use, said converter element from contamination with material from the resist layer.

5 The present invention further relates to a method for transferring a pattern onto a resist on a substrate, using a lithography system comprising a converter element for receiving light and converting said light in a plurality of electron beams to be directed towards and focused on a substrate to be processed, said plurality of electron beams being used to define a pattern in a resist layer on said substrate, the method comprising
10 the step of applying a metal film on said resist to protect, during transfer of said pattern, said converter element from contamination with material from the resist layer, said metal film being applied in an electron-transparent thickness.

 In an embodiment, said thin foil comprises at least one conductive surface. In such an embodiment it is possible to combine two possible functions of the foil. First,
15 the foil prevents material from the resist layer from reaching the converter element, and furthermore it may provide a well-defined plane of equal electric potential. Also, the conductive surface may prevent local charging of the foil. In a further embodiment, the thin foil is a thin metal foil. This metal foil can have a thickness of 1 – 200 micron. The thin metal foil may consists of gold, copper or molybdenum. In another embodiment,
20 the thin foil comprises a semiconductor layer, like silicon. The semiconductor layer can in turn be provided with a thin conducting film. e.g. a metal film. This film can either be a continuous layer, for instance having a thickness of between about 0.3-30 nm, or may have holes at the locations of the electron beams, or may have a thickness of between about 0.3-30 nm at the locations of the electron beams.

25 The holes in the foil provide locations, which are more transparent to the electron beams. In one embodiment, the holes are through holes or perforations.

 The preferred location of the holes is at the position of the electron beam sources. Especially when a foil of a larger thickness is used, said hole locations do not cause too much energy loss. In an embodiment, the holes are though holes or
30 perforations through the entire foil. If such a foil provided with holes is used, it was found that the beams should be kept within the inner 5-15 %, specifically about 10 % of the hole. Considering the divergence of the beam in practice, this leads to a relation between the size of the holes and the thickness of the foil. When, for instance, the

divergence of a beam is 30 mrad and the holes are 1 micron in diameter, this would lead to a maximum thickness of about 3-4 microns. An additional advantage of the foil provided with holes is that it offers the possibility to reduce electro-optical aberrations, specifically spherical aberrations. Furthermore, it makes it possible to choose a foil of greater thickness. In order for the lithography system to have a resolution better than 0,1 micron, the conductive layer of the foil should be very flat: better than 20 nanometers. This also means that the foil has to be rigid, or well clamped in the lithography system in order to cope with the electrostatic forces, which can influence the shape of the foil.

In order to prevent deformation, the foil can be provided with support bars. These bars can either be attached to the foil or be formed integrally with the foil. Especially when for instance silicon is used for the foil, it is well possible to produce a foil which is provided with support bars. These bars in one embodiment run across the foil, between the holes. The bars can be provided on either side of the foil, or on both sides. The foil, for instance silicon, can furthermore be provided with a conductive layer. For instance a metal layer can be applied. The metal layer can either be applied on the surface of the foil. As the metal layer preferably is as flat as possible, this would as for support bars on only one side of the foil.

The metal layer can be applied as a continuous layer all over the foil. The metal layer should, however, interfere as little as possible with the electron beams. In practice, this would require the layer to be as thin as 0,3-30 nm at the locations of the electron beams. An alternative would be to apply the metal layer only on the surface on the parts of the foil where there are no electron beams, or to thin the layer up to the required thickness at the locations of the electron beams.

In another embodiment of the lithography system of the invention, the protective foil is supported by the converter element. In an embodiment thereof, the converter element is a field-emission photo cathode array having spacers between its tips. In a further embodiment thereof the spacers are located between the tips or needles of said photo cathode array, the spacers being at least as high as the needles and being made of substantially non-conductive material, and support the foil. In even a further embodiment thereof, the area of the ends of the spacers is larger than the area of the basis of the spacers. In general, the field-emission photo cathode will be a semiconductor photo cathode. In a further embodiment thereof, the spacers at their end

are provided with a conductive layer, the conductive layers of the spacers being electrical conductively interconnected. In this way, it is possible to define a plane of constant potential. This can also reduce electro-optical aberrations like spherical aberrations. In one embodiment, the spacers hold a continuous conductive layer having openings at the positions of the tips of the needles. The continuous layer may be provided at a level above the tips of the needles. In another embodiment the conducting surface comprises a grid of electrodes. An advantage of the latter embodiment would be that every electron beam could be adjusted separately.

In an embodiment of the lithography system, wherein the protective foil is supported by the converter element, the converter element being a semiconductor gated field-emission photo cathode array.

In another embodiment of the invention, a metal film is deposited on top of the resist layer, said metal film being substantially transparent to said plurality of electron beams. Said metal film prevents resist material to leave the resist layer. Furthermore, it prevents a charge built up on said resist layer. Such a film may be deposited using known techniques. The thickness of said metal film is between 0.3 and 30 nm.

In an embodiment of the lithography system described, it is furthermore provided with means for providing a first electric potential V1 to said converter plate, a second electric potential V2 to said thin foil, and a third electric potential V3 to said substrate, wherein the electric field between the converter plate and the foil is substantially larger than the electric field between the foil and the substrate.

The invention further pertains to a lithography system comprising a source of charged particles and means for transferring a pattern onto a resist layer provided on a substrate using charged particles from said source of charged particles, wherein a metal film is provided on the surface of the resist layer in order to prevent material from the resist layer reaching said source of charged particles, said metal film having a for said charged particles transparent thickness.

The invention further pertains to a lithography system comprising a source of charged particles and means for transferring a pattern onto a resist layer provided on a substrate using charged particles from said source of charged particles, wherein said

lithography system is provided with a protective foil between said source of charged particles and said resist layer for preventing material from said resist layer from reaching said source of charged particles, wherein said lithography system is furthermore provided with transportation means to replace a first surface of said protective means by a second, fresh surface of said protective means.

The invention further pertains to a method for transferring a pattern onto a resist layer provided on a substrate in a lithographic process, wherein a metal film is provided on top of the resist layer before the pattern is transferred onto the resist layer. The metal film, in a very simple way, prevents material from the resist layer from influencing the process of the transferral of the pattern onto the resist layer. The thickness of the metal film should be such that the film is transparent for the electrons.

The invention further pertains to a substrate, processed by any of the described embodiments.

The lithography system may be provided with transportation means to replace a first surface of said protective foil, by a second, fresh surface of said protective foil. In this way it is avoided that energy loss or other disadvantages effects occur. In one embodiment thereof, said transportation means of said protective foil further comprises a first coil winder and a second coil winder, and the thin foil is arranged to be transported from said first coil winder to said second coil winder to replace said first surface of said thin foil by said second, fresh surface of said thin foil.

In another embodiment with transportation means, said transportation means of said protective foil comprise a stage, said thin foil being clamped on said stage, said stage being arranged for displacements to replace said first surface of said thin foil by said second, fresh surface of said thin foil. In an embodiment, said stage comprises piezoelectric actuators.

In the embodiments with transportation means, said thin foil is continuously transported at a predetermined rate in order to prevent said material attached to the present surface of the foil from influencing the performance of the lithography system.

Several of the described embodiments may also be combined, thus making it possible to fulfil all the requirements to obtain a lithography system which can produce

a pattern having details smaller than 0.1 micron on a substrate.

Brief description of the drawings

The invention will now be explained with reference to some drawings which are only intended to illustrate the invention and not to limit its scope of protection.

Figure 1 shows schematically a lithography system according to the prior art in which the field emitter photo cathode array can be used.

Figure 2 shows an example of a scanning direction of pixels on a substrate to be lithographed.

Figure 3 shows a Scanning Electron Microscope (SEM) image of a p-type silicon substrate with an array of tips.

Figure 4 shows schematically the operation of a semiconductor field emission array as shown in Figure 3 in a MAPPER set-up.

Figure 5 shows schematically the contamination process of the field emitter sources.

Figure 6 shows the setup of figure 4 provided with a first type of thin foil.

Figure 7 shows the setup of figure 4 provided with a second type of thin foil.

Figure 8 shows a lithographic system comprising a substrate provided with a metal film on the resist layer.

Figure 9 shows a SEM image of a semiconductor gated field-emission photocathode array as converter element.

Figure 10 shows schematically another means for protecting the converter element.

Figure 11 shows schematically yet another means for protecting the converter element.

Description of preferred embodiments

First of all a description of such a system as described in WO98/54620 is given. To that end, reference is made to Figure 1.

The background of the system described in WO98/54620 is as follows.

Imagine that there is provided a known deep-UV lithography tool (i.e., wavelength 193 nm or less) for the 0.13 μm generation with a "traditional" 4 x mask

for obtaining the 0.1 μm generation. Then, at a substrate surface, each 0.4 μm "pixel" of a mask is focused to a spot of 0.13 μm . Since the distance between pixels at the substrate must be 0.1 μm , there is a mixing of information between neighbouring pixels because the spots of 0.13 μm overlap each other. If we could sharpen up this 0.13 μm spot, this machine would be ready for the 0.1 generation. The sharpening up, or enhancement of resolution, cannot be done after the mixing of information has occurred.

According to one embodiment described in WO98/54620 only one pixel of the mask is illuminated. Then there is only an isolated spot of 0.13 μm at an imaginary substrate plane. At the location of the spot in the imaginary substrate plane a converter element, for example in the form of a photocathode of size 0.1 μm , or a photocathode with a metallic aperture of diameter of 0.1 μm on top, is positioned. Such a photocathode provides an electron source that may have a diameter of 0.1 μm or smaller. The photocathode that is obtained in this way is imaged with magnification factor 1 onto the substrate in a real substrate plane spaced from the photocathode. This can be done either with acceleration inside a magnetic field or with a small accelerating electrostatic lens. The next step is to move the mask, e.g., 0.4 μm in order to illuminate an adjacent pixel on the mask while, at the same time, movable the substrate 0.4/4 = 0.1 μm in order to have the adjacent pixel on the substrate written. In such a way, the mask pattern is transferred to the substrate with the required resolution.

However, it would take a long time to write patterns on a whole substrate with this single beam. The principle of pattern definition is the same when many pixels are written simultaneously. Therefore, a multiple beamlet embodiment can also be used. In theory, the distance between separate beams at the substrate surface needs only to be as much as the point spread function. In practice, certainly when electrostatic focusing is used, the fabrication technology of the photocathode/lens array will determine the minimum distance. The number of beams is estimated to be in the order of 10^6 - 10^8 .

Such a multiple beamlet embodiment is shown in Figure 1. A light source (not shown) produces a light beam 13, preferably in deep UV. The light beam 13 impinges on a micro lens array 1 having lenses 2. The light beam 13 is as it were divided in beamlets 12, of which only one is shown for the sake of clarity. However, in practice there may as much as 10^7 - 10^8 beamlets 12. The lens 2 focuses the beamlet 12 on a mask 3 with spots of, e.g., 400 nm diameter. Each light beamlet 12 leaving the mask 3

passes a demagnifier 14, which is schematically indicated by lenses 4 and 5 and an aperture 6. However, other types of demagnifiers known from the prior art may be used instead. By the demagnifier 14 the beamlets 12 are focused on a converter plate 7 having converter elements 8 of which only one is indicated. If, as disclosed by
5 WO98/54620, the converter plate 7 is constituted by a photocathode having a plurality of apertures a plurality of electron beamlets 15 (only 1 being shown in Figure 1) is generated. The electron beamlet 15 originates from the aperture and passes through focusing means, indicated schematically by a lens 9. Finally, the electron beamlet 15 impinges on the substrate 10 in substrate plane 11.

10 The mask 3 may be moved in the direction of arrow P1 and the substrate in the direction of arrow P2. If the mask 3 is, e.g., moved 0.4 μm the substrate must be shifted 0.1 μm . Pixels could be arranged at random on the substrate 10. In an embodiment shown in Figure 2, the substrate pixels are arranged in lines and columns and the scanning direction SCAN differs from the direction of the lines of pixels.

15 The resolution is enhanced by sharpening up pixel by pixel, using a photocathode with very many apertures. This known technology is called "Multiple Aperture Pixel by Pixel Enhancement of Resolution" or "MAPPER" technology. It can be thought of as traditional projection lithography in which the mask information is split up and transferred to the substrate sequentially. It can also be thought of as
20 multiple micro-column lithography in which the electron sources are blanked by the mask.

In accordance with PCT/NL00/00657, which text is incorporated herein by reference as if fully set forth, the converter plate 7 comprises a field emission array as shown in Figure 3. Figure 3 shows a plurality of tips on a p-doped silicon substrate.
25 The image has been made by means of a Scanning Electron Microscope (SEM). The silicon substrate was sized 5 mm x 5 mm. 81 x 81 tips were etched on the substrate surface. The tips shown were spaced about 8 μm whereas their height was about 4 μm . Of course, these figures are only examples. To further enhance the resolution on the substrate 10 to be processed, it is envisaged that the tips may be located closer to one
30 another than 8 μm .

The front surface from the tips, from which the electrons leave the silicon, have a diameter of preferably less than 100 nm, even more preferably less than 50 nm.

Figure 3 shows emitter tips with a conical shape. However, the invention is not

limited to such a shape. The tips may have a rectangular shape or an other cross sectional shape, or a spherical shape.

A structure as shown in Figure 3 has the following characteristics:

- ♦ field emission is limited by the availability of electrons in the operating regime;
- 5 ♦ electrons are excited from the valence band in the conduction band by photons from the impinging beamlets 12;
- ♦ tunnel probability approaches 1;
- ♦ due to field penetration in the tips the sources are less sensitive for pollution than metallic emitters.

10 Figure 4 shows the operation of the semiconductor field emission array 7 in more detail. The array 7 comprises a supporting substrate 17, e.g., made of Pyrex, but any other suitable material can be used. The supporting substrate must be made from a material that has a very low absorption factor for the wavelength of the light beamlets 12. For instance, when UV light is used the material may be quartz. On top of the
15 supporting substrate 17 a semiconductor point array layer 16 is provided, preferably made of p-doped silicon. However, by applying another semiconductor material the bandgap between the valence band and the conduction band may be tuned to the wavelength of the light beamlets 12 used.

The structure shown in Figure 4 is used in the transmissive mode, i.e., light
20 beamlet 12 impinges on the supporting substrate 17. The material used for the supporting substrate must be transparent to the wavelength of the light used. The photons from the light travel through the supporting substrate 17 and reach the semiconductor layer 16 where they will generate free electrons. The electrons 15 leave the silicon layer substantially at the front surface of the tips 19. An external (constant)
25 electric and magnetic field 18 accelerate the emitted electrons towards the wafer 10 and focus them on the wafer 10 to be processed. The electrical and magnetic fields are preferably directed in parallel from the silicon layer 16 towards the substrate 10.

Figure 5 shows schematically a contamination process of the field emitter sources. The converter plate 7 is positioned above the substrate 10 to be processed. The
30 substrate's surface is covered by a resist layer 20, which is used for pattern definition as known by persons skilled in the art. The electrons 15 emitted from the converter plate 7 are accelerated towards the substrate 10. Due to the interaction of the electrons 15, having relatively high energy due to the acceleration, and the resist layer, the resist

layer 20 degrades. Molecules 21 are released, in case a polymeric resist is used, due to cracking. The molecules traverse the vacuum between the substrate and the converter plate 7, and are adsorbed at the surface of the plate 7. The presence of adsorbed molecules 21 or other contamination on the surface of the converter plate 7 changes the emission properties of the converter plate, which, in turn, affects its quality.

Figure 6 shows the layout already described in figure 4, in which between the converter element 16 and the wafer 10 a thin foil 100 is provided with holes 101 at the locations of the electron beams 15 according to the present invention. In this embodiment, the thin foil is provided with supporting bars 102 which extend between the holes. The bars are added in order to prevent deformation of the foil. The foil can be made of silicon.

In figure 7, the foil 100 is provided with a conductive film 103 on its surface. This film can either be very thin, e.g. 0.3-30 nm overall, or have holes at the positions of the electron beams. It may also be possible to use a film which has thinned parts at the locations of the electron beams. The electron beams should be influenced, e.g. scattered or distorted, as little as possible.

Figure 8 shows a substrate 10 provided with a resist layer 20, and on top of the resist layer a metal film 30. This metal film 30 may be deposited using vapour deposition or other means known in the art. In the figure, the substrate is placed in a lithography system using a converter plate. The substrate may also be used in other lithographic systems. The metal film may also provide a defined potential plane. The thickness of this film typically is about 0.3-30 nm.

Figure 9 shows a SEM image of a top view and cross-cut of a semiconductor gated field-emission photocathode array. In such a photocathode array spacers are provided around the needles from which tips electrons are emitted. In one embodiment, The spacers are made of non-conductive or electrically insulating material, for instance silicon oxide. In the embodiment shown in the figure, the spacers fully surround the needles. In a further embodiment, the ends of the spacers are provided with a conductive layer, in a way to leave small openings around the tips of the needles. In this way the layer may provide a well defined plane of equal potential. In a further embodiment thereof, the spacers are higher than the needles. This provides the opportunity to manipulate the electron beams and improve the performance. In a further embodiment, the openings are as small as possible, preferably having a diameter

smaller than 0.5 micron or even smaller than 0.1 micron, in order to reduce the spreading of the electron beams.

The spacers may be provided on the converter element after the needles are produced, or may be remainders of the etching process creating the needles.

5 Figure 10 shows schematically an alternative embodiment of protective means for protecting the converter element for preventing the contamination of the surface of the converter plate 7. The converter plate 7 is positioned above the substrate 10 to be processed. The substrate's surface is covered by a resist layer 20, which is used for pattern definition as known by persons skilled in the art. The electrons 15 emitted from the converter plate 7 are accelerated towards the substrate 10.

10 Between the converter plate 7 and the substrate 10 a movable foil device 23 is located. The movable foil device 23 comprises a movable foil 24, a first coil winder 25 and a second coil winder 26. The foil 24 is located closely above the substrate 10. The first and second coil winders 25, 26 are located at positions displaced relative to the
15 converter plate 7 in the vertical direction in such a way that movements of parts of the multiple beam embodiment (Fig. 1) are not obstructed. They may be arranged to be driven by a motor (not shown) automatically or, alternatively, by hand. In this way the foil is transported.

20 The foil material has a typical thickness of 1–200 micron and is transparent for the energetic electrons 15. Such a foil may consist of e.g., gold, copper or molybdenum. At the same time, the foil prevents that cracked molecules 21 and/or other material released from the resist layer 20 and/or other contamination present in the system, from reaching the converter plate 7 and the emitter tips 19 by an earlier adsorption of the molecules on the foil 24.

25 The size of the foil surface is such that the area of the converter plate 7 as projected on the foil surface is smaller than the area of the foil surface. No line of sight exists between the surface of the resist layer 20, being exposed to the electron beamlets 12, and the converter plate 7. After a predetermined period of time, the foil 5 can be moved to refresh the portion of the foil 5 that has become contaminated by cracked
30 molecules 21. Thus, a fresh portion of the foil 5 becomes available between the converter plate 7 and the substrate surface level.

The foil 24 may be located, substantially, at focal distance from the tips 19 of the converter plate 7. The electron beamlets 12, coming in at the surface of the foil 24,

are thus focused on the foil as to prevent scattering at that point and defocusing at the substrate surface level.

Between the converter plate 7 and the foil area 24 a first accelerating potential V1 is maintained, and between the foil area 24 and the substrate surface (whatever layer being present) a second potential V2 is maintained. Preferably, the first potential V1 is much lower than V2 in order to obtain the best possible focusing of the beamlets on the foil. The second potential V2 has a higher level to reduce the kinetic energy of the electrons, and the energy transfer to the substrate 10 (i.e., to reduce heating). As an example, the accelerating potential V1 may be about -10 kV and the second potential V2 may be about 0 V.

By transportation of foil 24 from the first coil winder 25 to the second coil winder 26 the foil surface between converter plate 7 and substrate 10 can be refreshed as desired. Also, some foil materials may be cleaned by an in-situ plasma of e.g., oxygen and reused a few times before transportation to refresh the foil.

Figure 11 shows schematically an alternative embodiment of figure 10. In this alternative embodiment, the movable foil device 23 uses a mechanism 27 for displacing the foil parallel to the substrate surface to refresh the foil above it. It is conceivable to use a substantially planar foil that may be shifted in one direction (or in two, preferably orthogonal directions) to provide a clean foil area 24 under the converter plate 7 as needed. The foil is clamped on a stage 27 which provides the necessary displacements to have a fresh foil area under the converter plate 7. The movement of the foil 24 by the stage 27 may be controlled in any suitable way known in the art, e.g., by piezoelectric actuators 28, 29.

It is to be understood that the above description is included to illustrate the operation of the preferred embodiments and is not meant to limit the scope of the invention. The scope of the invention is to be limited only by the following claims. From the above discussion, many variations will be apparent to one skilled in the art that would yet be encompassed by the spirit and scope of the present invention.

Claims

1. A lithography system comprising a converter element for receiving light and converting said light in a plurality of electron beams to be directed towards and
5 focused on a substrate to be processed, said plurality of electron beams being used to define a pattern in a resist layer on said substrate, wherein said lithography system is provided with a protective thin foil located between said converter element and said substrate and having holes at the positions of the electron beams and being arranged to protect, in use, said converter element from contamination
10 with material from the resist layer.
2. The lithography system according to claim 1, characterized in that said protective thin foil comprises at least one conductive surface.
- 15 3. The lithography system according to claim 3, characterized in that said thin protective foil is a thin metal foil.
4. The lithography system according to claim 4, characterized in that said thin metal foil has a thickness of about 1-200 micron.
- 20 5. The lithography system according to claim 1, wherein said protective thin foil comprises supporting means.
6. The lithography system according to claim 5, wherein said supporting means
25 comprise support bars attached to or formed integrally with said protective thin foil.
7. The lithography system according to claim 1, characterized in that said protective thin foil comprises a semiconductor layer having holes at the positions of the electron beams.
- 30 8. The lithography system according to claim 7, characterized in that said semiconductor layer comprises supporting bars formed integrally with said

semiconductor layer.

9. The lithography system according to claim 7, wherein said protective thin foil comprises a thin conductive layer on the surface of said semiconductor layer.

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10. The lithography system according to claim 7, wherein said protective thin foil comprises a continuous metal film on the surface of said semiconductor layer, said metal film having a thickness of about 0-30 nm at the positions of the electron beams.

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11. The lithography system according to claim 1, wherein said protective thin foil is supported by the converter element.

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12. The lithography system according to claim 11, wherein the converter element comprises a semiconductor field-emission photo cathode array comprising semiconductor tips.

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13. The lithography system according to claim 12, wherein said support comprises spacers located between said tips of said photo cathode array, the spacers being at least as high as the tips, and being made of substantially non-conductive material.

14. The lithography system according to claim 13, wherein the area of the ends of said spacers is larger than the area of the basis of the spacers.

25

15. The lithography system according to claim 13 or 14, wherein the supported protective thin foil comprises at least one conducting surface.

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16. The lithography system according to claim 15, wherein said conducting surface is a metal surface layer.

17. The lithography system according to claim 16, wherein said metal surface layer is a continuous metal layer of less than 30 nm thickness at the positions of said electron beams.

18. The lithography system according to claim 17, wherein said conducting surface comprises a grid of electrodes.
- 5 19. The lithography system according to any one of the preceding claims, wherein a second protective element is deposited on top of said resist layer on said substrate, said second protective element being substantially transparent to said plurality of electron beams.
- 10 20. The lithography system according to claim 19, wherein said second protective element comprises a metal film.
21. The lithography system according to claim 20, wherein said metal film has a thickness of about 0,3-30 nm.
- 15 22. The lithography system according to any one of the preceding claims, furthermore provided with means for providing a first electric potential (V1) to said converter plate, a second electric potential (V2) to said thin foil, and a third electric potential (V3) to said substrate, wherein the electric field between the converter plate and the foil is substantially larger than the electric field between the foil and the substrate.
- 20 23. A lithography system comprising a source of charged particles and means for transferring a pattern onto a resist layer provided on a substrate using charged particles from said source of charged particles, wherein a metal film is provided on the surface of the resist layer in order to prevent material from the resist layer reaching said source of charged particles, said metal film having a for said charged particles transparent thickness.
- 25 24. The lithography system of claim 23, wherein said metal film has a thickness of between about 0.3-30 nm.
- 30 25. A method for transferring a pattern onto a resist on a substrate, using a lithography system comprising a converter element for receiving light and converting said light

in a plurality of electron beams to be directed towards and focused on a substrate to be processed, said plurality of electron beams being used to define a pattern in a resist layer on said substrate, the method comprising the step of applying a metal film on said resist to protect, during transfer of said pattern, said converter element from contamination with material from the resist layer, said metal film being applied in an electron-transparent thickness.

26. The method of claim 24, wherein said metal film is applied with a thickness of between about 0.3-30 nm.

Amended claims

[Received by the International Bureau on 22 January 2003 (22.01.03):
original claims 1 – 26 have been replaced by amended claims 1 - 18(3 pages)]

1. A lithography system comprising a source of charged particles for providing a plurality of electron beams to be directed towards and focused on a substrate to be processed, said plurality of electron beams being used to define a pattern in a resist layer on said substrate, wherein said lithography system is provided with a protective thin foil located between said source of charged particles and said substrate, said protective thin foil having holes at the positions of the electron beams and being arranged to protect, in use, said source of charged particles from contamination with material from the resist layer.
2. The lithography system according to claim 1, characterized in that said protective thin foil comprises at least one conductive surface.
3. The lithography system according to claim 3, characterized in that said thin protective foil is a thin metal foil.
4. The lithography system according to claim 4, characterized in that said thin metal foil has a thickness of about 1-200 micron.
5. The lithography system according to claim 1, wherein said protective thin foil comprises supporting means.
6. The lithography system according to claim 5, wherein said supporting means comprise support bars attached to or formed integrally with said protective thin foil.
7. The lithography system according to claim 1, characterized in that said protective thin foil comprises a semiconductor layer having holes at the positions of the electron beams.

8. The lithography system according to claim 7, characterized in that said semiconductor layer comprises supporting bars formed integrally with said semiconductor layer.
- 5 9. The lithography system according to claim 7, wherein said protective thin foil comprises a thin conductive layer on the surface of said semiconductor layer.
10. The lithography system according to claim 7, wherein said protective thin foil comprises a continuous metal film on the surface of said semiconductor layer, said
10 metal film having a thickness of about 0-30 nm at the positions of the electron beams.
11. The lithography system according to any one of the preceding claims, wherein the source comprises a semiconductor field-emission photo cathode array comprising
15 semiconductor tips.
12. The lithography system according to claim 9, wherein said conducting surface comprises a grid of electrodes.
- 20 13. The lithography system according to any one of the preceding claims, wherein a second protective element is deposited on top of said resist layer on said substrate, said second protective element being substantially transparent to said plurality of electron beams.
- 25 14. The lithography system according to claim 19, wherein said second protective element comprises a metal film.
15. The lithography system according to claim 20, wherein said metal film has a thickness of about 0,3-30 nm.
- 30 16. The lithography system according to any one of the preceding claims, furthermore provided with means for providing a first electric potential (V1) to said converter plate, a second electric potential (V2) to said thin foil, and a third electric potential

(V3) to said substrate, wherein the electric field between the converter plate and the foil is substantially larger than the electric field between the foil and the substrate.

17. The lithography system according to any one of the preceding claims, wherein said
5 foil is located closely above the substrate at a focal point of the electron beams.
18. The lithography system according to any one of the preceding claims, wherein said foil stands free from said source of charged particles.

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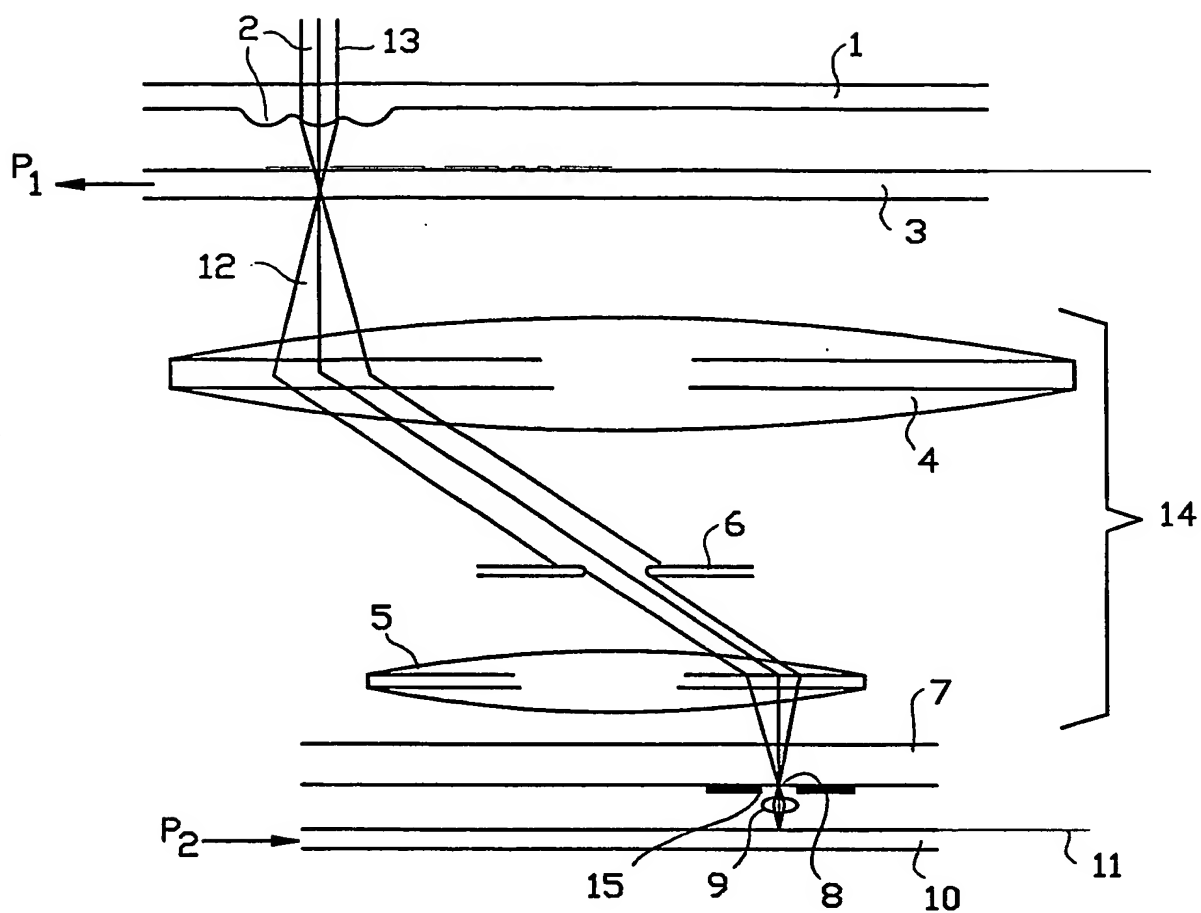


FIG. 1

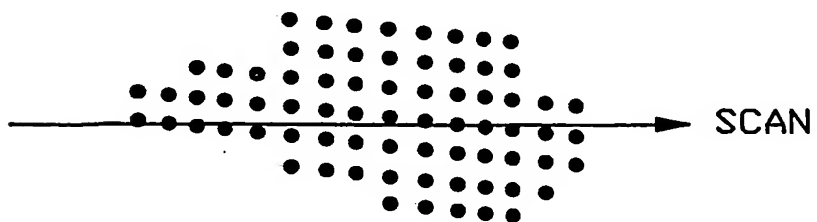


FIG. 2

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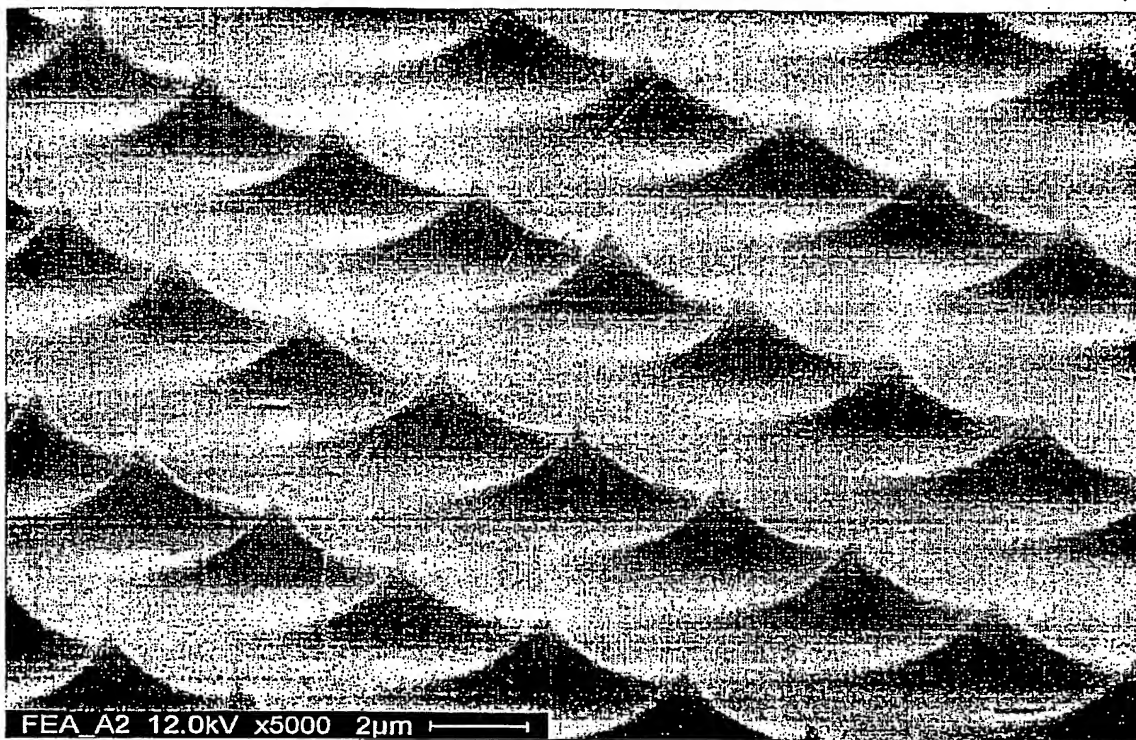


FIG. 3

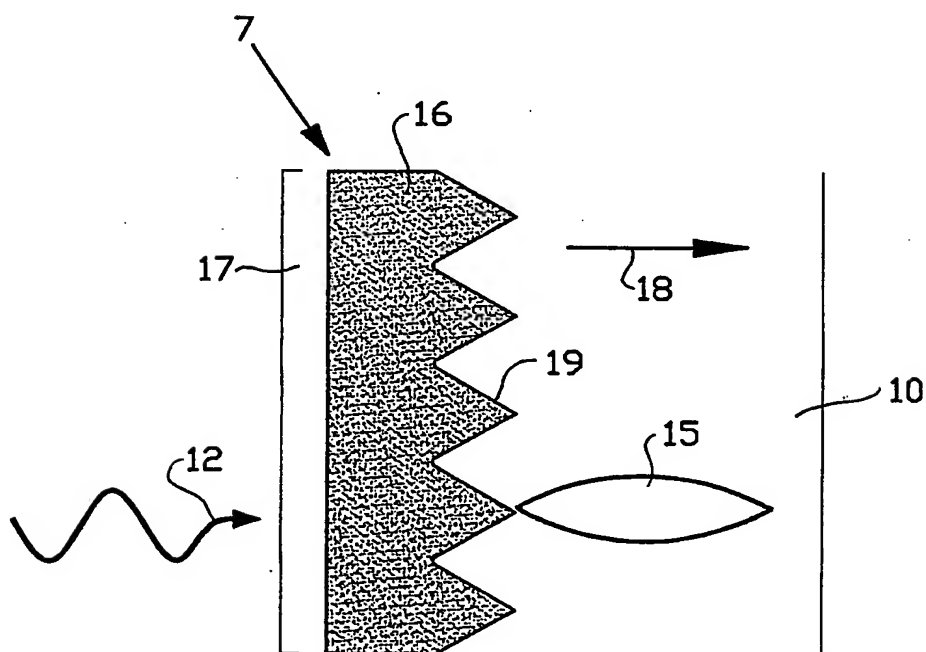


FIG. 4

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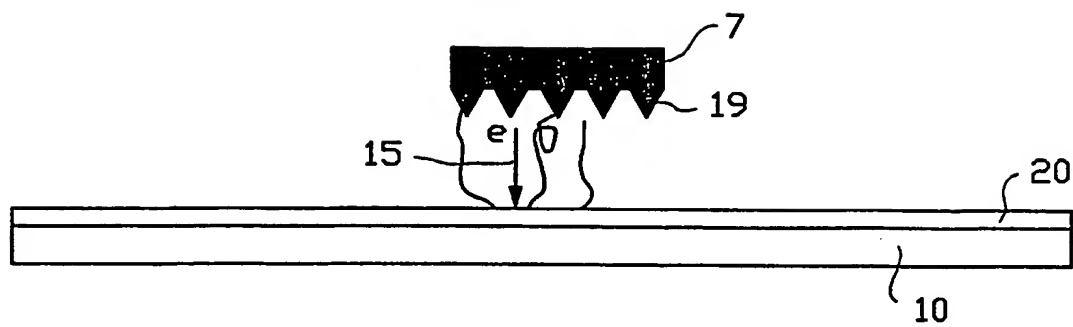


FIG. 5

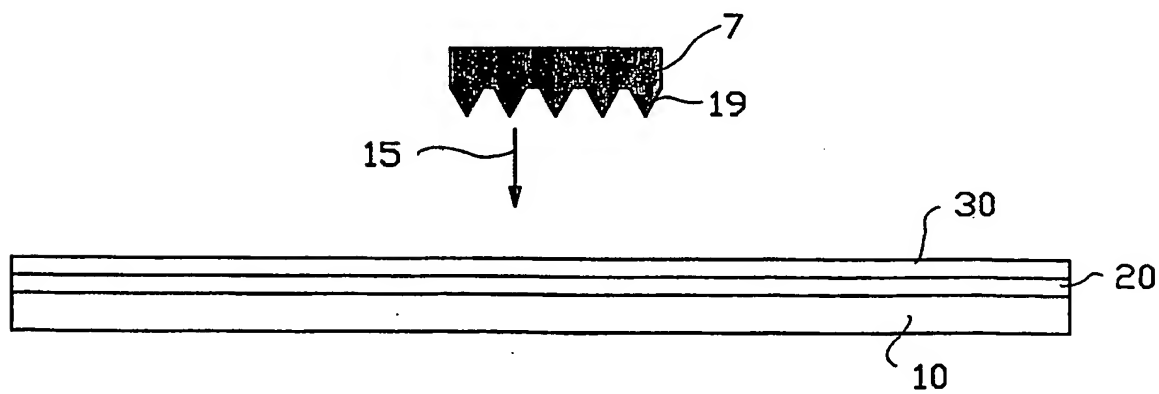


FIG. 8

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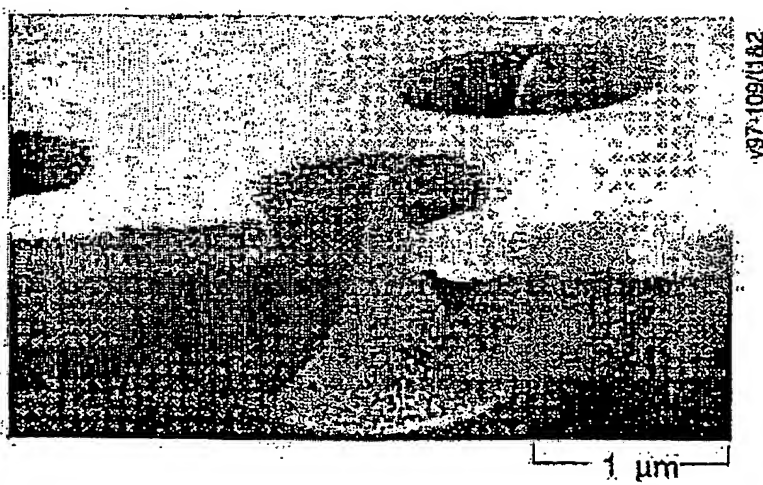
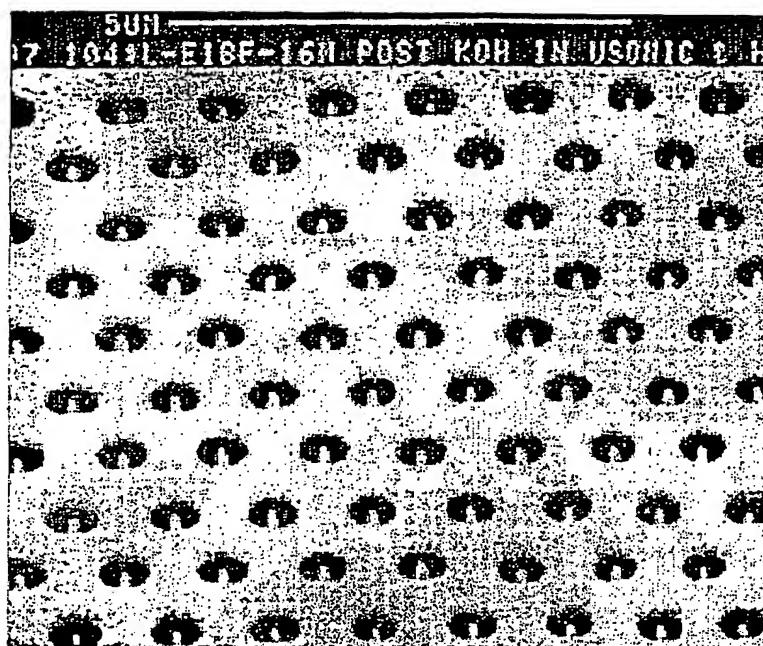


FIG. 9

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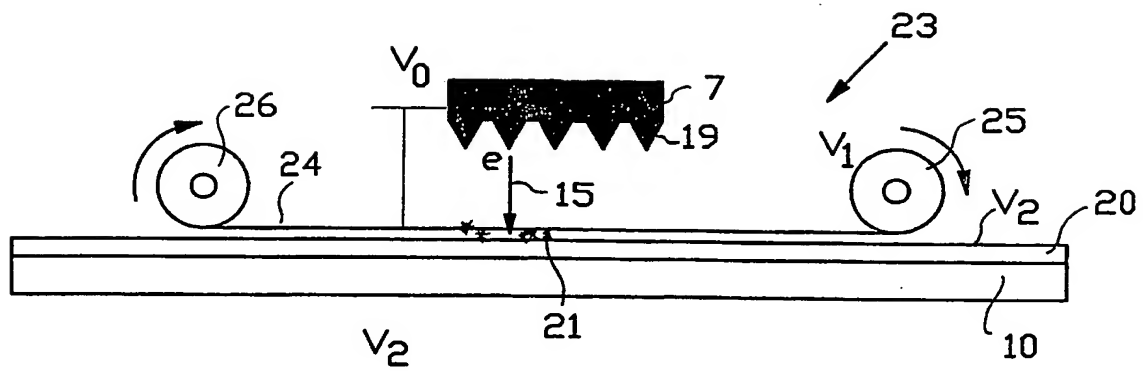


FIG. 10

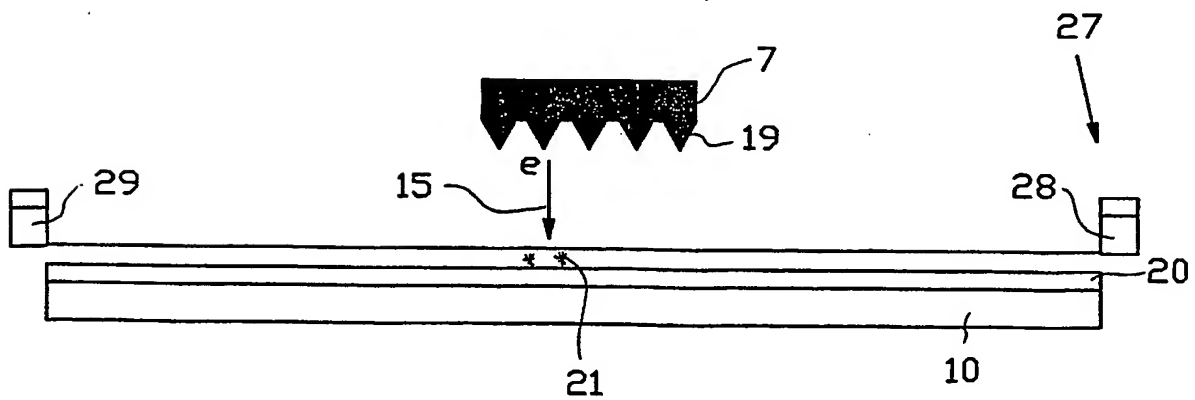


FIG. 11

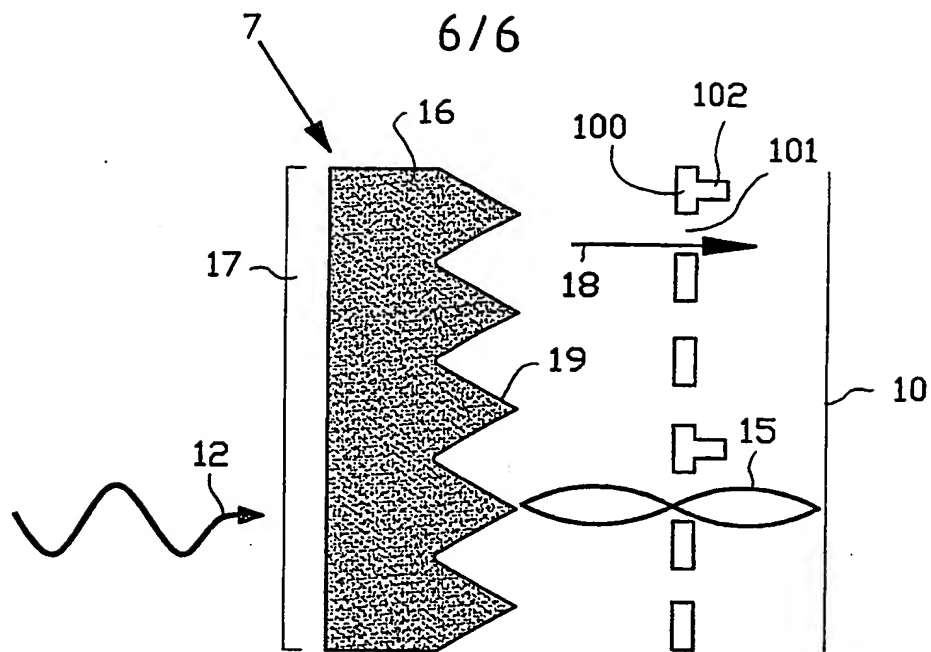


FIG. 6

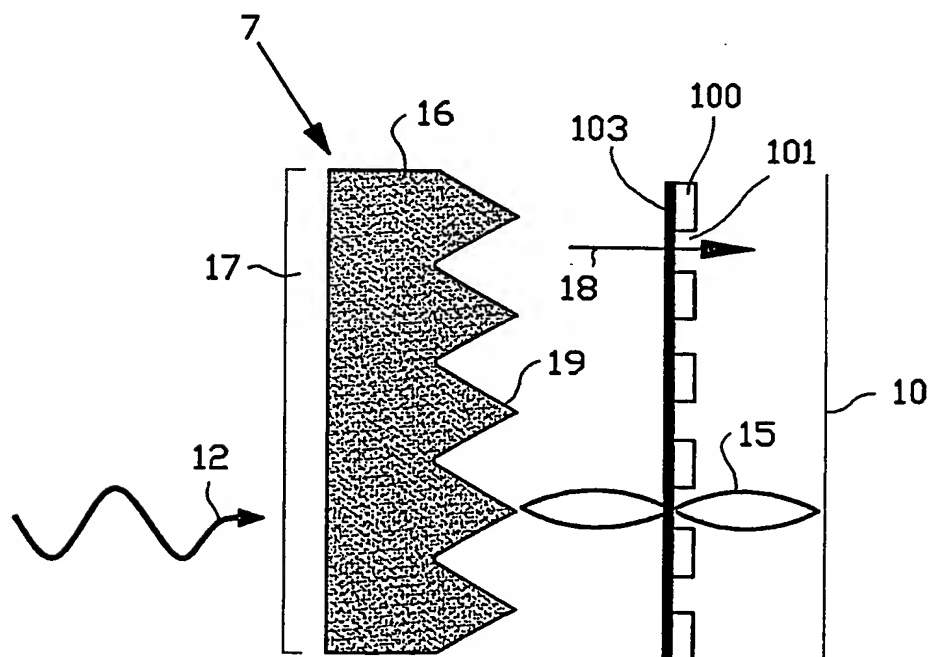


FIG. 7

INTERNATIONAL SEARCH REPORT

national Application No
PCT/NL 02/00541A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 H01J37/317 G03F7/20

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7 H01J G03F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	EP 0 605 964 A (AT & T CORP) 13 July 1994 (1994-07-13) column 4, line 19 - line 25 column 7, line 15 - column 8, line 26 figures 1,2 ---	1-3,11
X	US 4 695 732 A (WARD RODNEY) 22 September 1987 (1987-09-22) column 2, line 36 - column 6, line 10 figures ---	1-5,22
A	EP 0 334 334 A (FUJITSU LTD) 27 September 1989 (1989-09-27) page 5, line 52 - page 6, line 15 figure 2 --- -/--	1

☒ Further documents are listed in the continuation of box C.☒ Patent family members are listed in annex.

* Special categories of cited documents :

- "A" document defining the general state of the art which is not considered to be of particular relevance
- "E" earlier document but published on or after the international filing date
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- "&" document member of the same patent family

Date of the actual completion of the international search

3 October 2002

Date of mailing of the international search report

17/12/02

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Authorized officer

Aguilar, M.

INTERNATIONAL SEARCH REPORT

International Application No
PCT/NL 02/00541

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 4 902 930 A (VAN DER MAST KAREL D) 20 February 1990 (1990-02-20) cited in the application column 2, line 58 -column 3, line 8 column 3, line 35 -column 6, line 3 figure 1 -----	1

INTERNATIONAL SEARCH REPORT

International application No.
PCT/NL 02/00541

Box I Observations where certain claims were found unsearchable (Continuation of item 1 of first sheet)

This International Search Report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:

1. ☐ Claims Nos.:
because they relate to subject matter not required to be searched by this Authority, namely:
2. ☐ Claims Nos.:
because they relate to parts of the International Application that do not comply with the prescribed requirements to such an extent that no meaningful International Search can be carried out, specifically:
3. ☐ Claims Nos.:
because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).

Box II Observations where unity of invention is lacking (Continuation of item 2 of first sheet)

This International Searching Authority found multiple inventions in this international application, as follows:

see additional sheet

1. ☐ As all required additional search fees were timely paid by the applicant, this International Search Report covers all searchable claims.
2. ☐ As all searchable claims could be searched without effort justifying an additional fee, this Authority did not invite payment of any additional fee.
3. ☐ As only some of the required additional search fees were timely paid by the applicant, this International Search Report covers only those claims for which fees were paid, specifically claims Nos.:
4. ☒ No required additional search fees were timely paid by the applicant. Consequently, this International Search Report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.:

1-22

Remark on Protest

- ☐ The additional search fees were accompanied by the applicant's protest.
- ☐ No protest accompanied the payment of additional search fees.

This International Searching Authority found multiple (groups of) inventions in this international application, as follows:

1. Claims: 1-22

Protective thin film with holes at the positions of the electron beams for protecting the light to electron beam converting element of a lithography system

2. Claims: 23-26

Deposition of a metal film on a resist layer in charged particle lithography

INTERNATIONAL SEARCH REPORT

International Application No

PCT/NL 02/00541

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
EP 0605964	A	13-07-1994	US 5395738 A	07-03-1995
			CA 2107451 A1	30-06-1994
			DE 69324560 D1	27-05-1999
			DE 69324560 T2	23-09-1999
			EP 0605964 A2	13-07-1994
			ES 2132199 T3	16-08-1999
			JP 6283466 A	07-10-1994

US 4695732	A	22-09-1987	GB 2159322 A	27-11-1985
			DE 3574521 D1	04-01-1990
			EP 0161723 A2	21-11-1985
			JP 60254726 A	16-12-1985

EP 0334334	A	27-09-1989	JP 1241123 A	26-09-1989
			JP 1251613 A	06-10-1989
			JP 2002610 A	08-01-1990
			DE 68922929 D1	13-07-1995
			DE 68922929 T2	22-02-1996
			EP 0334334 A2	27-09-1989
			KR 9301493 B1	02-03-1993
			US 5023462 A	11-06-1991

US 4902930	A	20-02-1990	EP 0312653 A1	26-04-1989
			JP 1159955 A	22-06-1989
